



CMOS High Efficiency On-chip Power Management (Analog Circuits and Signal Processing)

John Hu, Mohammed Ismail

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This book will introduce various power management integrated circuits (IC) design techniques to build future energy-efficient “green” electronics. The goal is to achieve high efficiency, which is essential to meet consumers’ growing need for longer battery lives. The focus is to study topologies amiable for full on-chip implementation (few external components) in the mainstream CMOS technology, which will reduce the physical size and the manufacturing cost of the devices.

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